

● PRINTER RUSH ●

(PTO ASSISTANCE)

Application : <u>09/632235</u>	Examiner : <u>Do</u>	GAU : <u>2193</u>
From : <u>N/CB</u>	Location : <u>(IDC)</u> FMF FDC	Date : <u>11/4/06</u>
Tracking # : <u>LPM 09/632235</u> Week Date : <u>10-31-05</u>		

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449	_____	<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS	_____	<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM	_____	<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW	_____	<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW	_____	<input type="checkbox"/> Other
<input checked="" type="checkbox"/> DRW	<u>8-4-00</u>	
<input type="checkbox"/> OATH	_____	
<input type="checkbox"/> 312	_____	
<input type="checkbox"/> SPEC	_____	

[RUSH] MESSAGE: Attn.: Chief Draftsperson

① Figures 1 and 2 have electronic dates over
Figure label number.

② Figures 2 and 4 have holes within illustrations.

③ Figure 3 label number is within the illustration.

Thank You,
N/CB

[XRUSH] RESPONSE: 1/12/6

DRAWINGS CORRECTED

INITIALS: LAM

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.
 REV 10/04

Figure 1

Typical Prior Art Processor Architecture

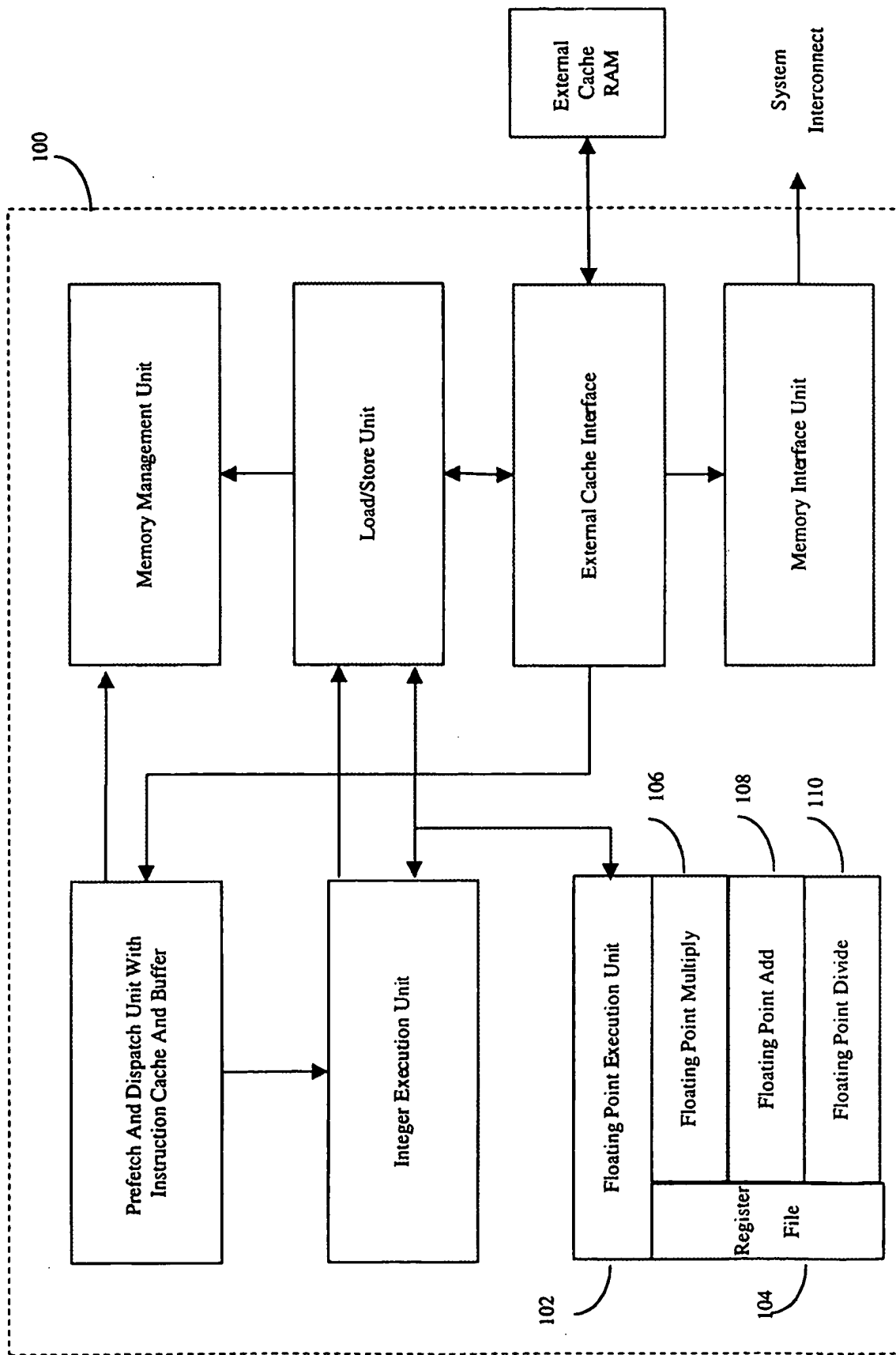


Figure 2
Floating Point Execution Unit:
Block Diagram Of Relevant Functionality
(Prior Art)

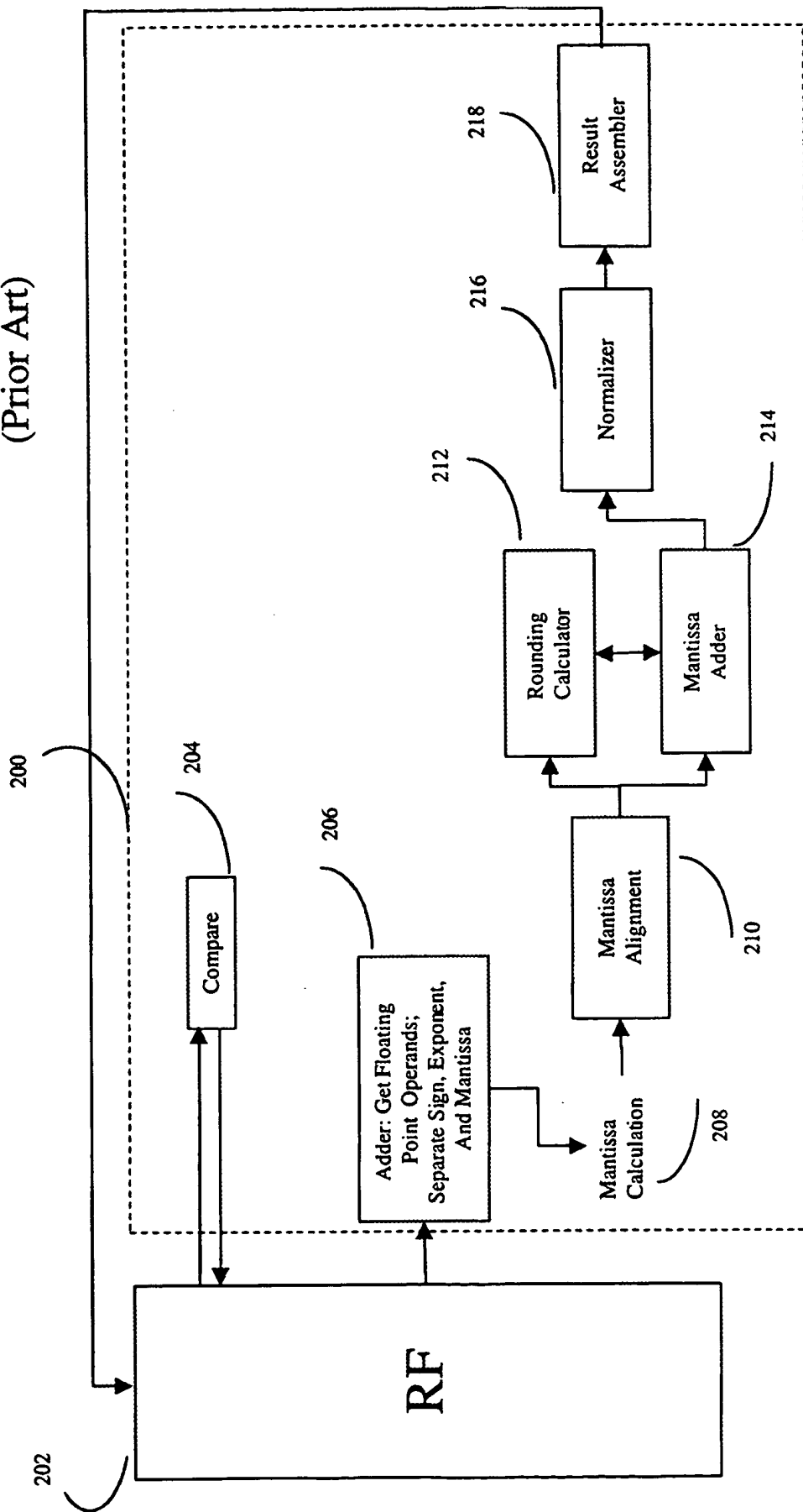


Figure 3
Prior Art Functional Pipeline

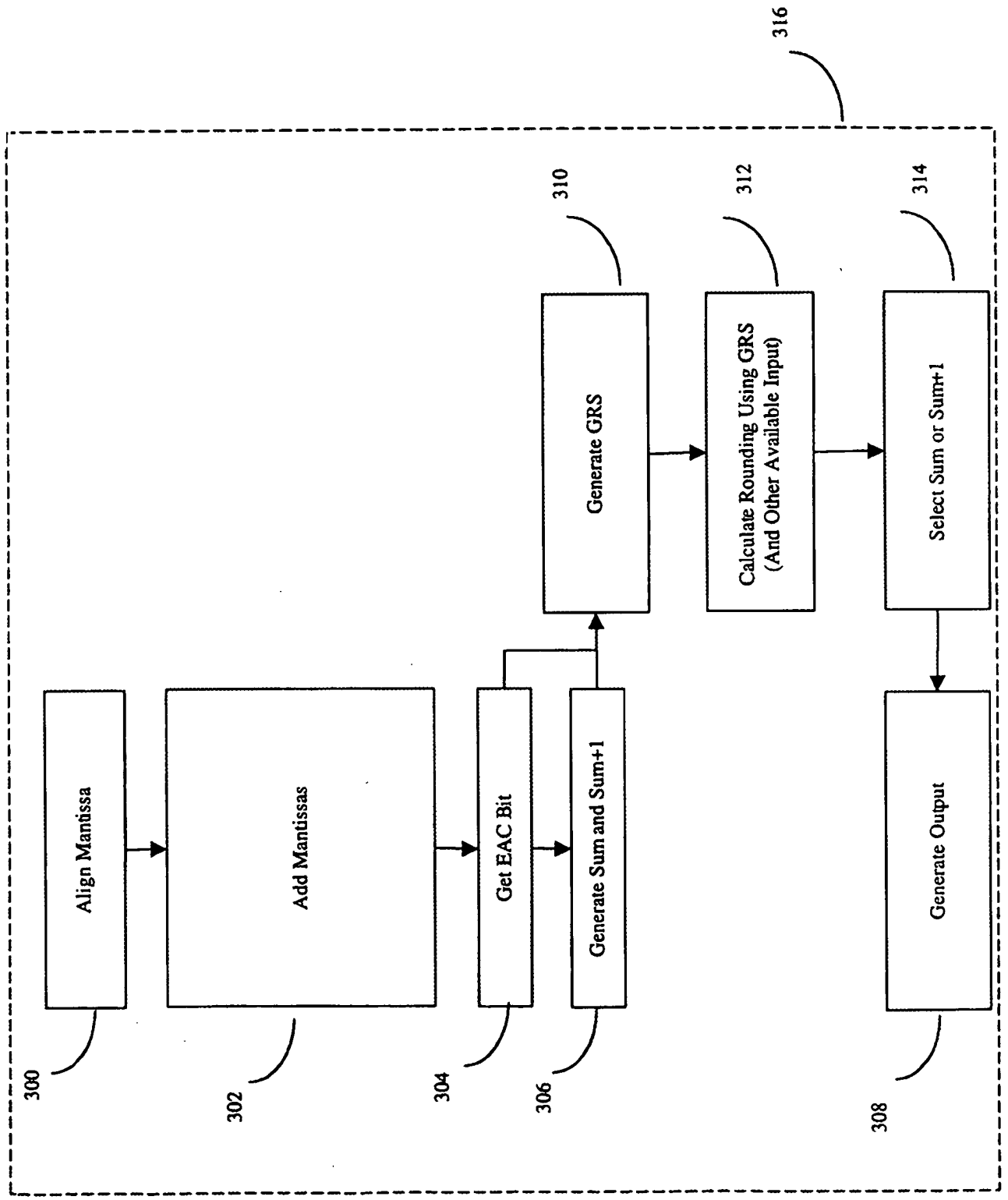


Figure 4
Block Diagram Of Relevant Portion Of A Floating
Point Execution Unit With Present Invention

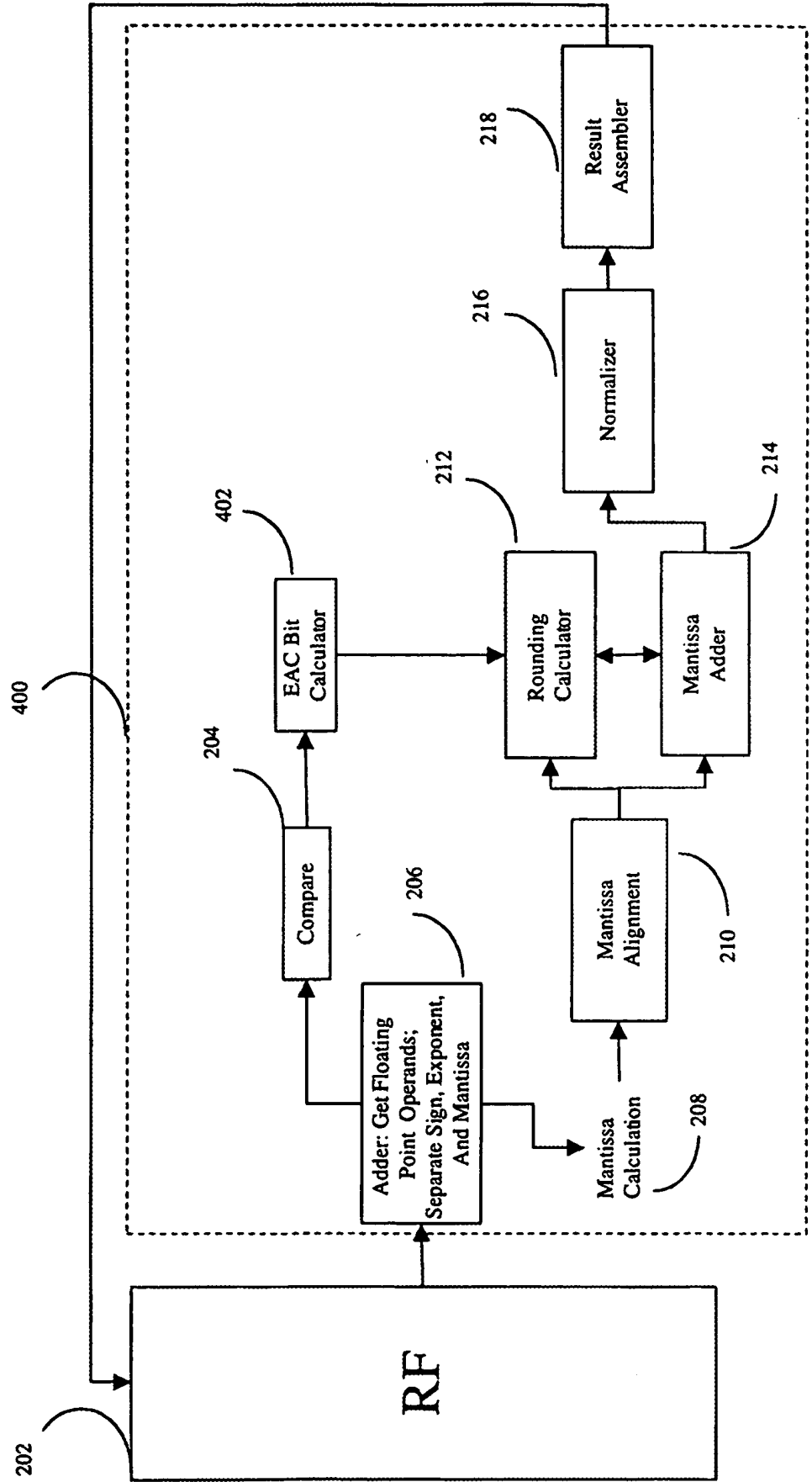


Figure 5
Functional Pipeline
With Present Invention

